

CIRCULAR

[Academic Year:2013-14, Semester-I]

ACADEMIC REGULATIONS for M.Tech(R13)-REGULAR

For theory subjects **25 MARKS SHALL BE AWARDED BASED ON THE INTERNAL EVALUATION** and during the semester two (2) mid-term examinations will be conducted. Each mid-term examination will be conducted for a total duration of **120 Minutes with**

1. **PART-A as compulsory question for 10 marks and(1Q)**
2. **PART-B with 3 questions to be answered out of 5 questions and each question for 5 marks.(5Q)**

Incharge of Exams

PRINCIPAL

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SREE CHAITANYA COLLEGE OF ENGINEERING: KARIMNAGAR
EXAMINATION BRANCH

Date: 19-11-2013

I Year-M.TECH I – Semester(R13) I-MID Exams Time Table

BRANCH	DATE&DAY			TIME:10:30AM - 12:30PM		
	25-11-13FN	26-11-13FN	27-11-13FN	28-11-13FN	29-11-13FN	30-11-13FN
CIVIL(SE)	Computer Oriented Numerical Methods	Theory of Elasticity and plasticity	Theory and Analysis of plates	Advanced Reinforced Concrete Design	Concrete Technology	Soil Dynamics and foundation Engineering
CIVIL(HVE)	Highway Infrastructure Engineering	Urban Transportation Planning	Traffic Engineering	Pavement Material Characterization	Concrete Technology	Project Management
EEE(PE&ES)	Advanced Power System Analysis	Advanced Power System Protection	Modern Control Theory	Renewable Energy Systems	HVDC Transmission	High Voltage Engineering
EEE(PE)	Machine Modeling and Analysis	Power Electronic Converters-I	Modern Control Theory	Power Electronic Control of DC Drives	HVDC Transmission	Special Machines
MECH(DFM)	Advanced Mechanics of Solids	Materials Technology	Precision Engineering	Design for Manufacturing and Assembly	Special Manufacturing Process	Advanced CAD
MECH(TE)	Advanced Optimization Techniques & Applications	Advanced Thermodynamics	Advanced Heat & Mass Transfer	Advanced Fluid Mechanics	Solar Energy Technology	Advanced I.C. Engines
ECE(VLSI SYSTEM DESIGN)	VLSI Technology and Design	CMOS Analog Integrated Circuit Design	CPLD and FPGA Architectures and Applications	CMOS Digital Integrated Circuit Design	Digital System Design	Micro Controllers for Embedded System Design
ECE(WMC)	Advanced Data Communications	TCP/IP and ATM Networks	Advanced Digital Signal Processing	Image and Video Processing	Random Processes and Time Series Analysis	Satellite Communications
CSE(CF&IS)	Mathematical Foundation for Cyber Security	Applied Cryptography	Network and Wireless Security	Biometric Systems and Biometric Image Processing	Distributed and Cloud Computing	Web Security
CSE	Advanced Data Structures and Algorithms	Computer System Design	Advanced Operating Systems	Distributed Systems	Software Process and Project Management	Software Architecture and Design Patterns

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EXAMINATION BRANCH

Date: 19-11-2013

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[Academic Year:2013-14, Semester-I]

All the **I-Year-M.TECH I-Semester** students are informed that the **I- MID-Term** Examinations are scheduled from **25-11-2013**.

I MID-Term Exams Time Table

BRANCH	DATE&DAY					
	25-11-13FN	26-11-13FN	27-11-13FN	28-11-13FN	29-11-13FN	30-11-13FN
CIVIL(SE)	Computer Oriented Numerical Methods	Theory of Elasticity and plasticity	Theory and Analysis of plates	Advanced Reinforced Concrete Design	Concrete Technology	Soil Dynamics and foundation Engineering
CIVIL(HVE)	Highway Infrastructure Engineering	Urban Transportation Planning	Traffic Engineering	Pavement Material Characterization	Concrete Technology	Project Management
EEE(PE&ES)	Advanced Power System Analysis	Advanced Power System Protection	Modern Control Theory	Renewable Energy Systems	HVDC Transmission	High Voltage Engineering
EEE(PE)	Machine Modeling and Analysis	Power Electronic Converters-I	Modern Control Theory	Power Electronic Control of DC Drives	HVDC Transmission	Special Machines
MECH(DFM)	Advanced Mechanics of Solids	Materials Technology	Precision Engineering	Design for Manufacturing and Assembly	Special Manufacturing Process	Advanced CAD
MECH(TE)	Advanced Optimization Techniques & Applications	Advanced Thermodynamics	Advanced Heat & Mass Transfer	Advanced Fluid Mechanics	Solar Energy Technology	Advanced I.C. Engines
ECE(VLSI SYSTEM DESIGN)	VLSI Technology and Design	CMOS Analog Integrated Circuit Design	CPLD and FPGA Architectures and Applications	CMOS Digital Integrated Circuit Design	Digital System Design	Micro Controllers for Embedded System Design
ECE(WMC)	Advanced Data Communications	TCP/IP and ATM Networks	Advanced Digital Signal Processing	Image and Video Processing	Random Processes and Time Series Analysis	Satellite Communications
CSE(CF&IS)	Mathematical Foundation for Cyber Security	Applied Cryptography	Network and Wireless Security	Biometric Systems and Biometric Image Processing	Distributed and Cloud Computing	Web Security
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