**VENUMADHAV.J** Mobile: +919963102809

 M.Tech E-mail id: jvm.nani@gmail.com

Associate Professor

**Career objective:**

To support academic development and excellence in professional practice, teaching and learning, and other aspects of knowledge exchange and/or knowledge transfer to produce high quality outputs in line with the institution’s strategic ambitions.

**Total Experience in years : 10 years (Teaching/Industry)**

**Professional Summary:**

* Working as ***Associate Professor and PG (M. Tech) Department Coordinator in*** ***Sree Chaitanya College of Engineering****,* Karimnagar.
* Worked as **Department placement in-charge** at Sree Chaitanya College of Engineering karimnagar.
* Worked as **Department Academic in-charge** at Sree Chaitanya College of Engineering karimnagar.
* Worked as **Department Project (UG/PG) in-charge** at Sree Chaitanya College of Engineering karimnagar.
* Worked as an **Apprentice A.E** in **NPDCL from**26.07.03 to 25.07.04 Karimnagar.

**Job Profile as a Associate Professor:**

* Provides supervision and support for postgraduate and doctoral students to ensure successful completion of study to achieve faculty completion rates.
* Works with others to help promote a culture of innovation, continuous improvement, efficiency and value for money.
* Assisting the students in their Project Preparation as part of their academic requirement as prescribed by the University

**EDUCATIONAL QUALIFICATIONS:**

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **Sl. No.** | **Exam****passed** | **Month & year of passing** | **Board or University** | **Specialization**  | **% of Marks** | **Class / Division** |
|
| 1 | M.Tech | Nov 2009 | JNTU,Hyd | Power system (H.V.E) | 72% | Distinction |
| 2 | B.Tech | April 2002 | JITS-KNR | E.E.E | 60.5% | First |
| 3 | Inter | March 1996 | Board of Intermediate | M.P.C | 60.5% | First |
| 4 | SSC | March 1999 | BSE,jgl |  - | 76% | First |

 **PROJECT PROFILE:**

* **M.Tech :** Power Upgrading By Combining AC-DC Transmission By Using PSCAD/Simulation
* **B.Tech :**‘Order Reduction of Linear System’ Using an Error Minimization Technique

**SEMINARS, TRAINING PROGRAMMES, WORKSHOPS ATTENDED:**

|  |  |  |  |
| --- | --- | --- | --- |
| **S.No** | **Name of the Workshop/ Symposia /seminar etc** | **Name of the Organizer** | **Duration** |
| **from** | **to** |
| 1 | FDP on Teaching Ethics | Ramappa Engg College, Warangal | 28-02-2009 | 01-03-2009 |
| 2 | SCADA and its Applications | JITS-KNR | 18-12-2010 | 19-12-2010 |
| 3 | Research Methodology | JNTU-Nachupally | 3 week’s |  |

**Professional strengths:**

* Possess excellent communication skills as lecturer, teacher
* Possess excellent written and verbal communication skills
* Excellent interpersonal and teaching skills
* Ability to understand diverse learning needs of students.
* Possess good monitoring and management skills.
* Excellent coordinating skills with faculty, administration and students.

***PERSONAL DETAILS:***

 Name : VENUMADHAV.JERIPOTHULA

 Father Name : DURGAIAH

 Date of Birth : 12-07-1978

 Nationality : Indian

 Languages Known : English, Hindi, and Telugu

 Permanent Address  **:** H.NO.2-74,

 Kothapally (H)

 Karimnagar,

 Contact No. **:**9963102809

 I hereby declare that the above given information in correct.

Station:

Date:

(**VENUMADHAV.J**)